

MON960 Debug Monitor



- Supports Communication on Your Host Computer or a Terminal Through a Serial or Parallel Cable
- Runs on Cyclone Evaluation Boards, EP80960BB and PCI80960DP
- Requires 64K of ROM and Only 32K of System RAM
- Comes With the Complete Source and is Easily Ported to Custom Board Designs
- Communicates With Software Debuggers Such as Intel's GDB960 and DB960
- Supports Big-Endian Byte-ordering and Flash Memory Downloading
- Supports Code Download via PCI Bus

MON960 is a retargetable debug monitor that can help you design embedded systems based on the i960® processor.

The monitor resides on the board you are designing and lets you control the operation of the processor on the board. With the monitor, you can test your hardware by displaying and changing memory, by displaying and changing registers, and by disassembling memory. You can also test your software by downloading application programs, then stepping through the program, tracing values, and setting breakpoints.

Complete source code royalty free is provided with comprehensive retargeting instruction. Example code for porting to Intel evaluation boards, the Intel 82510 UART serial controller chip, and the 82C54 counter/timer are provided.

MON960 is included in the GNU/960 and CTOOLS960 software development toolsets. It is also available as a standalone product.

PROCESSORS SUPPORTED:
i960 Processor Family

CONTACT:
Local Intel Sales Office or Distributor
WWW: <http://www.intel.com/embedded/>

